



JH-M-Fill terbank

Passive LPF

Active LPF

LM317

use short wire on solder side
(not long loop around
components, as drawn)

SIL 8 Packages
are 4 * 2k2

2kHz
50k log

2.8kHz
50k log

4kHz
50k log

5.6kHz
50k log

HPF
50k log

DrvLink
+ 0 -

D_Out Input
D_Out OpaOut

DrvLink
+ 0 -